Code No.: 6116

(5)

## VASAVI COLLEGE OF ENGINEERING (Autonomous), HYDERABAD M.E. I Year (ECE) I-Semester (Make Up) Examinations, May-2015 (Embedded Systems & VLSI Design)

## **VLSI Technology**

Time: 3 hours Max. Marks: 70

Note: Answer ALL questions in Part-A and any FIVE questions from Part-B

## Part-A (10 X 2=20 Marks)

	Part-A (10 X 2=20 Marks)	
1.	Describe the different types of resistors that are realized on integrated circuits.  Discuss the advantages of miniaturization.	
	그리스 가게 가게 되고 있는데 얼마가 된 보다는데 그렇게 때가 내려왔다. 그리고 가게 되는데 가게 하게 되었다면 하고 있는데 되었다. 그 그렇게 되었다.	
3.	Explain why poly-silicon is preferred as the gate electrode for a MOSFET?  Describe the functions of SiO <sub>2</sub> layers in an IC.	
<ol> <li>4.</li> <li>5.</li> </ol>	Discuss the effects of oxygen contamination in Silicon during crystal growth.	
6.	Indicate two methods used for measuring thermal oxide thickness.	
7.	Indicate the advantages of CVD methods of deposition.	
8.	Compare the two different types of photo resists Used in lithography.	
9.	Discuss the role of annealing in VLSI processing.	
	Indicate the importance of silicides in VLSI fabrication.	
	Part-B (5 X 10=50 Marks)	
11.	a) Explain the structure (cross section and top view) of an nMOSFET in an n-well process	S
	with the help of neat sketches.	(5)
	b) Explain the different ways in which capacitors can be realized on integrated circuits.	(5)
12.	a) Describe briefly the process flow for the fabrication of bipolar ICs	(7)
	b) Discuss the extra steps needed to convert a CMOS process into a BICMOS process.	(3)
13.	<ul><li>a) Discuss the importance of epitaxial layers in IC fabrication.</li><li>b) Explain what is meant by solid state epitaxy and describe the process to carry out this</li></ul>	(4)
	process step.	(6)
14.	a) Describe the chemistries that are used for dry etching of SiO <sub>2</sub> and Si. b) Compare the different etching techniques from the point of view of selectivity and	(4)
	anisotropy.	(6)

take place and indicate the boundary conditions. (5)

16. a) Discuss the impurity segregation during the CZ crystal growth. (5)

b) A projection exposure system uses a lens of 0.4 numerical aperture and UV light of 365 nm wavelength. Calculate the resolution and the depth of field that can be obtained with this system. (5)

b) Discuss the conditions under which the constant source and limited source diffusions

15. a) Describe the atomic models of diffusion.

17. a) Explain how the impurity concentration and Junction depth are independently controlled in an Ion implantation process? (4

b) Describe the effect of wafer orientation on oxidation. Differentiate between linear and parabolic rates of oxidation. (6)

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